IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) Martin VORBACH et al. :

Cont. of

Serial No. 10/191,926

Ser. No. To be assigned

Filed Herewith

METHOD OF HIERARCHICAL CACHING OF For

> CONFIGURATION DATA HAVING DATAFLOW PROCESSORS AND MODULES HAVING TWO- OR MULTIDIMENSIONAL PROGRAMMABLE CELL

STRUCTURE (FPGAs, DPGAs, etc.)

Art Unit To be assigned

Examiner To be assigned :

Mail Stop Patent Application Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

SIR:

Prior to examination, please amend the above-identified continuation application as follows:

Amendments to the Abstract begin on page 2 of this paper.

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Claims are reflected in the listing of claims, which begins on page 5 of this paper.

Remarks begin on page 6 of this paper.